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Corrections to “An Improved Harmonics Mitigation Scheme for a Modular Multilevel Converter” [2019 147244-147255]

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Published in:
IEEE Access

DOI (link to publication from Publisher):
[10.1109/ACCESS.2020.2982797](https://doi.org/10.1109/ACCESS.2020.2982797)

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Publication date:
2020

Document Version
Publisher's PDF, also known as Version of record

[Link to publication from Aalborg University](#)

Citation for published version (APA):

Kumar, A. R., Deepa, T., Bhaskar, M. S., Subramaniam, U., Almakhlles, D., Padmanaban, S., & Nielsen, J. B-H. (2020). Corrections to “An Improved Harmonics Mitigation Scheme for a Modular Multilevel Converter” [2019 147244-147255]. *IEEE Access*, 8, 65351-65351. <https://doi.org/10.1109/ACCESS.2020.2982797>

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Received March 5, 2020, accepted March 19, 2020, date of publication March 23, 2020, date of current version April 17, 2020.

Digital Object Identifier 10.1109/ACCESS.2020.2982797

COMMENTS AND CORRECTIONS

Corrections to “An Improved Harmonics Mitigation Scheme for a Modular Multilevel Converter”

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This work was supported by the Renewable Energy Laboratory, Department of Communications and Networks Engineering, Prince Sultan University, Riyadh, Saudi Arabia.

In the above article [1], T. Deepa should have been listed as the second co-author of the article with the affiliation of (1): School of Electrical Engineering, Vellore Institute of Technology, Chennai 600127, India. The author's biography is also provided within this correction.

Additionally, the correct zip code of affiliation (1) should be 600127, and the correct statement on financial support acknowledgement should be as follows: “This work was funded by the Renewable Energy Laboratory, Department of Communications and Networks Engineering, Prince Sultan University, Riyadh, Saudi Arabia.” It is necessary to mention the nature of funding provided by Prince Sultan University and to note the correction in the spelling of the university in the same statement in the published manuscript.

REFERENCES

- [1] A. R. Kumar, M. S. Bhaskar, U. Subramaniam, D. Almakhles, S. Padmanaban, and J. Bo-Holm Nielsen, “An improved harmonics mitigation scheme for a modular multilevel converter,” *IEEE Access*, vol. 7, pp. 147244–147255, 2019.



T. DEEPA received the B.Tech. degree in electrical and electronics engineering from Manonmaniam Sundaranar University, Tirunelveli, and the M.Tech. degree in power system and the Ph.D. degree in process control from the College of Engineering, Anna University, Guindy. She is currently working as an Associate Professor with the School of Electrical Engineering, Vellore Institute of Technology, Chennai Campus. Her research interests include process control, control systems, and intelligent controllers.

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